

# Structure-determined thermoelectric properties of $\text{Bi}_2\text{Se}_3$ thin films deposited by vapour-solid technique.

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**Abstract**— In this work, a simple catalyst-free vapour-solid deposition method is applied for controlled obtaining of two types (planar and disordered) continuous  $\text{Bi}_2\text{Se}_3$  nanostructured thin films on different (fused quartz/glass, mica, graphene) substrates. Performed for the deposited thin films transport and thermoelectric characterization (type, concentration and mobility of the main charge carriers, Seebeck coefficient and power factor) showed that proposed deposition method allows to fabricate “low-doped”  $\text{Bi}_2\text{Se}_3$  thin films with power factor comparable and even higher than reported for the  $\text{Bi}_2\text{Se}_3$  thin films fabricated by molecular beam epitaxy technique.

## I. INTRODUCTION

Bismuth selenide ( $\text{Bi}_2\text{Se}_3$ ) is known as a narrow band gap semiconductor with excellent thermoelectric (TE) properties for near-room temperatures.  $\text{Bi}_2\text{Se}_3$  has a rhombohedral crystal structure in the space group  $D_{3d}^5$  ( $R\bar{3}m$ ) and consists of charge neutralized layers bonded together with weak Van der Waals forces along the c-axis. Each charge neutralized layer (quintuple layer, Q-layer) is formed by five covalently bonded atomic sheets (Se-Bi-Se-Bi-Se).

The main strategies for increase of the TE efficiency of materials include the use of numerous interfaces for efficient phonon scattering resulting in reduction of the lattice thermal conductivity, and the use of quantum confinement phenomena to increase the Seebeck coefficient of the material. For the best near-room temperature TE materials as bismuth chalcogenides, which are found to be topological insulators, quantum confinement introduced by the downsizing down to nanoscale was proposed an efficient method for simultaneous suppression of the thermal conductivity and enhancing of thermoelectrical power factor [1].

For the practical applications, the preferred method for bismuth chalcogenide thin films preparation is simple cost-effective catalyst-free vapour-solid deposition method using different substrates like Si, glass, mica [2], indium tin oxide covered glass [3] and graphene [4].

In this work, the catalyst-free vapour-solid deposition technique [5] is applied for controlled obtaining of two types of continuous nanostructured  $\text{Bi}_2\text{Se}_3$  thin films on different substrates like fused quartz/glass, mica and chemical vapour deposition (CVD) graphene. Planar thin films, consisting of coalesced nanoplates planar to the substrate surface, are suitable for both in-plane and through-plane measurements and layered devices. In turn, disordered thin films, consisting of both planar and oriented under random angles relative to the substrate surface nanoplates, have been reported useful for

sandwich-type devices [4], as well as are more suitable for sensor applications due to high surface-to-volume ratio. Structure, electron transport and thermoelectric properties of these thin films are determined and discussed.

## II. EXPERIMENTAL

### A. Deposition of the thin films.

$\text{Bi}_2\text{Se}_3$  nanostructured thin films were deposited by previously reported catalyst-free vapour-solid deposition method using quartz tube furnace (GCL-1100X, MTI Corp) [4]. Source material evaporation temperature was 585°C. Synthesis of thin films occurred without carrier gas flow except for obtaining of disordered  $\text{Bi}_2\text{Se}_3$  thin films on graphene surface, when the temporary gas flow (dynamic pressure 50-150 Torr) was introduced into the furnace tube during the cooling process for the temperature interval 535 – 475°C. Typical thicknesses of the deposited thin films are 100-200 nm and depend on the deposition time and amount of the source material.

### B. Substrate preparation

Quartz/glass substrates were washed in 2-propanol and distilled water, and dried under  $\text{N}_2$  flow. Muscovite mica sheets (Agar Scientific) were mechanically cleaved immediately before the deposition process. Monolayer graphene was prepared by chemical vapour deposition (CVD) method on a copper foil and transferred on the quartz/glass substrates using polymer-assisted technique described elsewhere [4].

### C. Investigation methods

Morphology, thickness, structure and composition of obtained  $\text{Bi}_2\text{Se}_3$  nanoplates and nanostructured thin films were inspected using field emission scanning electron microscope (SEM) Hitachi S-4800 equipped with an energy-dispersive X-ray (EDX) analyser Bruker XFLASH 5010 and atomic force microscope (AFM) Asylum Research MFP-3D.

Transport properties of the thin films were measured using Hall effect measurement system HMS-5000. Thermoelectric measurements were carried out for the temperature range 295 – 320 K by home-made device calibrated with Standard Reference Material 3451 for low temperature Seebeck coefficient (NIST).

## III. RESULTS AND DISCUSSION

As morphology of the nanostructured thin films deposited by catalyst-free vapour-solid technique is governed by the properties of the substrate surface, to obtain films  $\text{Bi}_2\text{Se}_3$  thin films of a certain structure deposition was made on three

different types of substrates: monolayer CVD graphene, mica and fused quartz or glass. At the beginning of the deposition process, the natural diffusion of the evaporated source material from the hot centre of the furnace tube results in random nucleation and further growth of  $\text{Bi}_2\text{Se}_3$  nanoplates directly on the substrate surface, which corresponds to Vollmer-Weber (island) growth mode. Due to the intrinsically anisotropic bonding nature, the most energetically favourable orientation of  $\text{Bi}_2\text{Se}_3$  nuclei is Q-layers parallel to the substrate surface. The further lateral growth of the  $\text{Bi}_2\text{Se}_3$  nuclei occurs symmetrically and faster than vertical due to the diffusion of adatoms to the edges of the nanoplates over its Se-passivated top surface [6]. The symmetry of the nanoplates may be affected by the anisotropy of the substrate surface (Fig. 1 a-c), rising from the presence of grain boundaries or dangling chemical bonds on the substrate surface. Here, this was used for obtaining of thin films consisting of nanoplates of a certain orientation.

### B. Planar thin films

Planar thin films can be obtained on the substrates not having dangling bonds on their surface (mica, graphene) under the following synthesis parameters: process pressure ~ 2-3 Torr, substrate temperature 330-380°C, no carrier gas flow. The nanoplates formed during the start phase of the deposition coalesce together forming continuous planar thin film (Fig. 1 a,b,d,e,f). Such thin films cannot be obtained on not-passivated fused quartz or glass substrates due to the influence of chemical surface defects originating from their structure [7]. The thin films that can be formed on the quartz/glass surfaces are discussed in the next section.

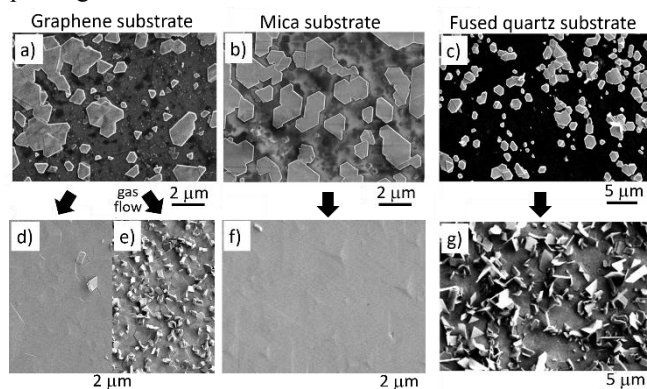


Fig. 1 a-b) SEM images of heteroepitaxial partially merged  $\text{Bi}_2\text{Se}_3$  nanoplates on graphene (a) and mica (b) surfaces; c) SEM image of  $\text{Bi}_2\text{Se}_3$  nuclei on glass surface; d-f) SEM images of planar (d – on graphene, f – on mica) and mixed (e – on graphene, g – on glass) continuous nanostructured  $\text{Bi}_2\text{Se}_3$  thin films.

### C. Disordered thin films.

Disordered  $\text{Bi}_2\text{Se}_3$  thin films can be easily obtained on the fused quartz of glass substrates under the same parameters as for planar thin films. The dangling bonds originating from the defects on quartz/glass surface are introducing defects in the crystalline structure of growing nanoplates and promoting formation of other phases of  $\text{Bi}_2\text{Se}_3$  crystals, resulting in formation of  $\text{Bi}_2\text{Se}_3$  thin films consisting of disordered nanoplates (Fig. 1 c,g).

Obtaining of disordered thin films on substrates like graphene or mica, promoting heteroepitaxial growth of  $\text{Bi}_2\text{Se}_3$  nanostructures, is a challenge. However, for the CVD graphene with grainy structure this can be achieved by the introduction of the temporary carrier gas flow with dynamic pressure starting from 50 Torr into the process tube during the  $\text{Bi}_2\text{Se}_3$  deposition (as described in the Experimental section). Presumably, the carrier gas flow causes rapid increase of vaporized source material concentration in the deposition area, thus promoting formation of vertical nanoplates at the facing grain boundaries edges of heteroepitaxial nanoplates. This is followed by formation of continuous nanostructured  $\text{Bi}_2\text{Se}_3$  thin film consisting of mix of planar as well as oriented under different angles relative to the substrate surface nanoplates (Fig. 1 e) [4]. It should be noted that the temporary gas flow with dynamic pressure up to 150 Torr does not promote formation of randomly oriented nanoplates on mica surface. This fact indirectly confirms that in the case of CVD graphene substrate the formation of randomly oriented nanoplates is promoted by the grain boundaries.

### D. Structural and electron transport properties of $\text{Bi}_2\text{Se}_3$ thin films.

The percentage of Bi and Se found in the thin films corresponds to stoichiometric  $\text{Bi}_2\text{Se}_3$  (40% Bi : 60% Se). Diffraction peaks of XRD patterns obtained for the planar and disordered  $\text{Bi}_2\text{Se}_3$  thin films (Fig. 2 a) can be indexed to the rhombohedral structure with cell units of  $a = b = 4.139 \text{ \AA}$  and  $c = 28.636 \text{ \AA}$  ([8], ref. code 00-033-0124). The observed at the XRD diffraction patterns of  $\text{Bi}_2\text{Se}_3$  thin films (0 0 l) peaks are significantly stronger than those for the standard powder diffraction pattern, while characteristic for bulk  $\text{Bi}_2\text{Se}_3$  (1010) and (110) diffraction peaks are lacking, but intensity of (015) peak is very low (Fig. 2 b). With an exception of (015) peak, the affiliation of all presented in the XRD patterns diffraction peaks to a group (0 0 3n) indicates nearly pure growth of the thin films in single-crystalline phase with c-axis oriented perpendicularly to the substrate.

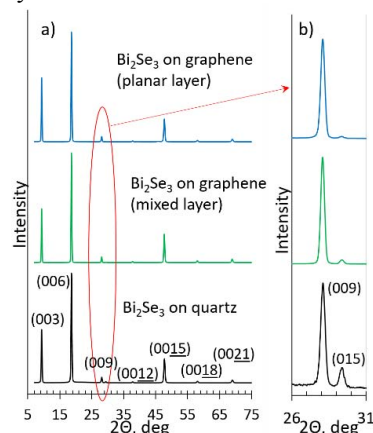


Fig. 2 Comparison between XRD patterns of continuous nanostructured  $\text{Bi}_2\text{Se}_3$  thin films deposited on quartz and graphene substrates.

Closer look at XRD patterns (Fig. 2 b) shows that the intensity of (015) diffraction peak for planar  $\text{Bi}_2\text{Se}_3$  thin film

deposited is negligible, while slightly raises for the disordered thin film deposited on graphene substrates, and becomes noticeable for the disordered  $\text{Bi}_2\text{Se}_3$  thin film deposited on quartz substrate. This corresponds to the SEM images of the disordered thin films (Fig. 1 e.g), showing higher proportion of oriented under different angles relative to the substrate surface nanoplates per unit area for the thin film deposited on quartz. At the same time, the intensity of (015) peak for the disordered  $\text{Bi}_2\text{Se}_3$  thin film deposited on quartz is still very low in comparison with other peaks, which may indicate that the disordered nanoplates are formed only on the top layer of the planar thin film.

Electrical transport data for all characterized samples are summarized in Table 1.

Table 1. Electrical transport and thermoelectric properties of continuous nanostructured  $\text{Bi}_2\text{Se}_3$  thin films deposited on different substrates.

No.	Substrate	Charge carrier concentration, $\text{cm}^{-3}$	Charge carrier mobility, $\text{cm}^2/(\text{V}\cdot\text{s})$	Conductivity S/cm	Absolute value of Seebeck coefficient $\mu\text{V}/\text{K}$	Power factor, $\mu\text{W}/(\text{cm}\cdot\text{K}^2)$
1	Quartz/glass	$-2.2\cdot 10^{18}$	510	182	182	6
2		$-3.3\cdot 10^{18}$	427	231	122	3.45
3		$-4.7\cdot 10^{18}$	356	271	74	1.45
4		$-1.95\cdot 10^{18}$	279	86.5	143	1.75
5		$-3.9\cdot 10^{18}$	339	211	209	9.2
6	Mica	$-2.35\cdot 10^{18}$	453	171	162	4.5
7		$-2.9\cdot 10^{18}$	441	207	227	10.7
8	Graphene	$3.6\cdot 10^{19}$	500	287	33	0.3
9		$4.2\cdot 10^{18}$	257	172	89	1.35
10		$2\cdot 10^{19}$	110	352	48	0.8
11		$1.3\cdot 10^{19}$	148	556	36	0.73
12		$1.25\cdot 10^{19}$	360	723	18	0.23

Planar and disordered thin films deposited on quartz/glass and mica substrates were found to be n-type due to native defects such as  $\text{Se}_{\text{Bi}}$  antisites and/or Se vacancies, acting as active n-type dopants [9]. Charge carrier concentrations in these thin films were in the range from  $\sim 2\cdot 10^{18}$  up to  $4.7\cdot 10^{18}\text{cm}^{-3}$  with mobilities varying between 120 and 510  $\text{cm}^2/(\text{V}\cdot\text{s})$ .

Presumably, such scatter of the experimental results origins from the different concentration of defects in the thin films. The highest mobility values showed by planar ( $\sim 450\text{cm}^2/(\text{V}\cdot\text{s})$ ) and disordered (425-510  $\text{cm}^2/(\text{V}\cdot\text{s})$ ) thin films are close to the values (320-1000  $\text{cm}^2/(\text{V}\cdot\text{s})$ ) reported for the “low-doped”  $\text{Bi}_2\text{Se}_3$  nanostructures with the level of native charged impurity disorder in the thin film in order of  $10^{13}\text{cm}^{-2}$  [10].

The different from “low-doped”  $\text{Bi}_2\text{Se}_3$  nanostructures values of charge carrier mobilities and concentrations may be related to the higher concentration of charge impurities and defects in the thin film. For instance, lower charge carrier mobility and at the same time higher charge carrier concentration (Table 1, samples 3 and 5), may be related to positively charged defects in the grain boundaries ( $\text{Se}_{\text{Bi}}$  antisites/Se vacancies), known to increase charge carrier concentration by producing of excess electrons. In turn, the lower carrier concentrations and at the same time lower mobility (Table 1, samples 1 and 4) may be addressed to increased concentration of defects (charge impurities on the

grain boundaries, vacancies) in the thin film, as well as to discontinuities in the thin films formed during the deposition.

The temperature dependence of charge carrier mobilities for disordered and planar  $\text{Bi}_2\text{Se}_3$  thin films (Table 1, samples 2 and 6) are shown in log-log scale in Fig. 3 a. It has a power law scaling ( $\sim T^n$ ) with exponents  $n \approx -0.42$  and  $-0.45$  for the disordered and planar thin films respectively. These exponents suggest that for the temperatures  $\geq 100\text{K}$  two scattering processes in these thin films may occur: electron-phonon ( $\sim T^{-3/2}$ ) scattering and scattering on interfacial charged defects ( $\sim T^{3/2}$ ), which may act as selective filter of low-energy minority charge carriers, thus improving the TE efficiency of the thin films [11].

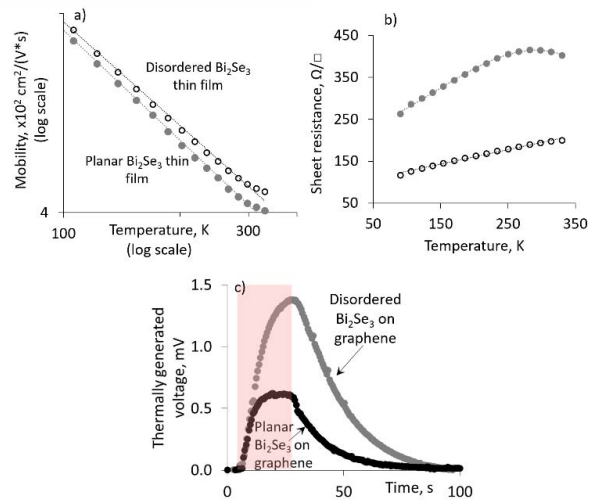


Fig. 3 a) Temperature dependence of the charge carrier mobility and b) temperature dependence of sheet resistance of planar (grey circles) and disordered (hollow circles)  $\text{Bi}_2\text{Se}_3$  thin films; c) thermally generated ( $\Delta T=16\text{K}$ ) in through-plane measurement configuration voltage of planar (black circles) and disordered (grey circles)  $\text{Bi}_2\text{Se}_3$  thin films deposited on graphene substrates. The temperature difference of 16 K was applied between the top and bottom graphene electrodes for 20 s (illustrated by the pink rectangular).

The temperature dependence of the sheet resistance showed metallic behaviour for disordered  $\text{Bi}_2\text{Se}_3$  thin film, while the planar  $\text{Bi}_2\text{Se}_3$  thin film demonstrated a hump with a maximum around 280K (Fig. 3b), which may indicate multiband transport typical for low-doped topological insulator nanostructures [5].

All  $\text{Bi}_2\text{Se}_3$  thin films deposited on graphene substrates showed p-type conductivity (Table 1) and higher in comparison with deposited on mica and quartz/glass substrates  $\text{Bi}_2\text{Se}_3$  thin films charge carrier concentrations ( $4.2\cdot 10^{18} - 3.6\cdot 10^{19}\text{cm}^{-3}$ ). However, the charge carrier mobilities of these samples varied between 110 and 500  $\text{cm}^2/(\text{V}\cdot\text{s})$  (Table 1, samples 8-12), which is comparable with the charge carrier mobilities of  $\text{Bi}_2\text{Se}_3$  thin films obtained on quartz/glass and mica substrates. Possibly, the p-type conductivity and higher charge carrier concentrations mirror the properties of the graphene substrates, accompanied by some impact from the  $\text{Bi}_2\text{Se}_3$  thin film, as the electrical contacts were made to the  $\text{Bi}_2\text{Se}_3$  thin films, while dominating charge carrier transport occurs through the graphene substrate due to its very low electrical resistance.

### E. Thermoelectric properties of continuous $\text{Bi}_2\text{Se}_3$ thin films.

Seebeck coefficient values and estimated power factors of the  $\text{Bi}_2\text{Se}_3$  thin films are listed in Table 1.

The average absolute values of a Seebeck coefficient at room temperature for  $\text{Bi}_2\text{Se}_3$  thin films (except ones deposited on graphene and discussed later in text) were respectively 130  $\mu\text{V}/\text{K}$  for the disordered thin films deposited on quartz/glass substrates (Table 1, samples 1–4) and 200  $\mu\text{V}/\text{K}$  for the planar thin films (Table 1, samples 5-7). These values exceed the absolute values of Seebeck coefficient reported for  $\text{Bi}_2\text{Se}_3$  nanostructures synthesized via solvothermal method (115  $\mu\text{V}/\text{K}$ ) [12] and  $\text{Bi}_2\text{Se}_3$  thin films with thickness 30 quintuple thin films grown by molecular beam epitaxy (104.3  $\mu\text{V}/\text{K}$ ) [13], and are 2-3 times higher than the absolute value of the Seebeck coefficient of the bulk  $\text{Bi}_2\text{Se}_3$  at room temperature (59  $\mu\text{V}/\text{K}$ ) [14]. Probably, the high values of Seebeck coefficient can be explained by selective low energy charge carriers filtering on the interfacial charged defects between the nanoplates composing the  $\text{Bi}_2\text{Se}_3$  thin films, similarly to the effect reported for n-type  $\text{Bi}_2\text{Te}_3$  few-layered flakes [11].

The power factors calculated for the “low-doped” disordered  $\text{Bi}_2\text{Se}_3$  thin films obtained on quartz/glass were 3.45-6  $\mu\text{W}/\text{cm}\cdot\text{K}^2$ , while planar  $\text{Bi}_2\text{Se}_3$  thin films obtained on mica showed values up to 10.7  $\mu\text{W}/\text{cm}\cdot\text{K}^2$  at room temperature, which is close and even higher than the power factors reported for the MBE grown  $\text{Bi}_2\text{Se}_3$  thin films (up to 6  $\mu\text{W}/\text{cm}\cdot\text{K}^2$ ) and pulsed laser deposition (up to 5.54  $\mu\text{W}/\text{cm}\cdot\text{K}^2$ ) methods [15]. Power factors of the disordered  $\text{Bi}_2\text{Se}_3$  thin films with higher amount of defects (1.45-1.75  $\mu\text{W}/\text{cm}\cdot\text{K}^2$ , Table 1, samples 3,4) are comparable with the power factors reported for  $\text{Bi}_2\text{Se}_3$  thin films grown by chemical solution (1-1.5  $\mu\text{W}/\text{cm}\cdot\text{K}^2$ ) [16].

The  $\text{Bi}_2\text{Se}_3$  thin films deposited on graphene showed Seebeck coefficients spread in the range 18-89  $\mu\text{V}/\text{K}$  (avg. 45  $\mu\text{V}/\text{K}$ ). Considering that Seebeck coefficient of monolayer CVD graphene is reported to be  $\sim 30$   $\mu\text{V}/\text{K}$  [17], it can be concluded that the result of in-plane measurements of the Seebeck coefficient  $\text{Bi}_2\text{Se}_3$  layer on the graphene surface is most likely the combination of thermoelectric properties of graphene and thermoelectric effect given by the  $\text{Bi}_2\text{Se}_3$  electrodes. Power factor of the  $\text{Bi}_2\text{Se}_3$  thin films deposited on graphene were spread in the range (0.23-1.35  $\mu\text{W}/\text{cm}\cdot\text{K}^2$ ).

It should be noted that the thermoelectric properties of the  $\text{Bi}_2\text{Se}_3$  thin films obtained on graphene were expected to be similar to the properties of  $\text{Bi}_2\text{Se}_3$  thin films deposited on mica. While the thermoelectric properties of  $\text{Bi}_2\text{Se}_3$  thin films deposited on graphene substrates may not be accurately measured in-plane, using graphene as a substrate for  $\text{Bi}_2\text{Se}_3$  deposition and simultaneously as a top or bottom electrode is a good solution for through-plane thermoelectric characterization of  $\text{Bi}_2\text{Se}_3$  nanostructured thin films. An example of through-plane measurements of the 200-nm thick planar and mixed  $\text{Bi}_2\text{Se}_3$  thin films deposited on the graphene substrate and using another graphene layer as top electrode is shown in Fig. 3 c. In this experiment, the temperature gradient of 16 K was applied between the top- and bottom

graphene electrodes for  $\sim 20$  s (illustrated in Fig. 3 c as pink rectangle).

The voltage-time curves (Fig. 3 c) illustrate thermally generated by the samples voltage. The curves show rapid increase of thermally generated voltage when the temperature difference is applied to the electrodes, followed by saturation and decrease with the decrease of the temperature gradient between the graphene electrodes. As it is seen from the Fig. 3 c, the enclosed between two graphene sheets  $\text{Bi}_2\text{Se}_3$  thin films showed pronounced response to the temperature difference of 16 K applied between the top and bottom electrodes. The non-planar thin film showed nearly twice higher thermoelectric response in comparison with planar thin film, which agrees with previous works on similar structures [4].

### ACKNOWLEDGMENT

This work was supported by the ERDF projects No 1.1.1.1/16/A/257 and 1.1.1.2/1/16/037.

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